

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	Unknown 10/721,437
				Filing Date	Unknown 11/24/03
				First Named Inventor:	WOHLMUTH, Walter A.
				Examiner name: Unknown Dao H. Nguyen	GROUP: Unknown 2818
Sheet	1	of	1	Attorney Docket Number	TRQ-00004

U.S. PUBLISHED PATENT APPLICATIONS

Examiner Initials*	Cited No. ¹	U.S. Patent Document Number	Kind Code ² (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Info. Appear
dlm	A01	2002-0171076	A1	Danzilio	11/21/02	

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Examiner Initials*	Cited No. ¹	U.S. Patent Document Number	Kind Code ² (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Info. Appear
dlm	A02	6,242,293	B1	Danzilio	6/5/01	
	A03	6,258,639	B1	Rohdin et al.	7/10/01	
	A04	6,307,221	B1	Danzilio	10/23/01	
	A05	6,521,961	B1	Costa et al.	2/18/03	
	A06	6,555,850	B1	Sakamoto et al.	4/29/03	
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

dlm	Toyoda et al., "An application of Pt-GaAs reaction to GaAs ICs," Inst. Phys. Conf. Ser. No. 63, Chapter 11, pp. 521-526 (1981)
dlm	Schulz et al., "Morphological Development During Platinum/Gallium Arsenide Interfacial Reactions," J. of Elect. Mat., 19, #6, pp. 581-589 (1990)
dlm	Ko, Dae-Hong and Robert Sinclair, "In-situ dynamic high-resolution transmission electron microscopy: application to Pt/GaAs interfacial reactions," Ultramicroscopy, 54, pp. 166-178 (1994)
dlm	Saka et al., "Low Standby Leakage Current Power Amplifier Module Made with Junction PHEMT Technology," 25 th IEEE GaAs IC Symposium Technical Digest, pp. 301-304 (2003)

EXAMINER	<u>T. Dao</u>	DATE CONSIDERED	09/16/05
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¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

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 First Named Inventor: Walter A. Wohlmuth
 Examiner: Unknown
 GROUP: 281 2818
 Attorney Docket Number TRQ-00004

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dh	A01	US-5,241,197	08/1993	Murakami et al.	
	A02	US-5,389,554	02/1995	Liu et al.	
	A03	US- 5,569,944	10/1996	Delaney et al.	
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	A05	US-6,034,386	03/2000	Ando	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document Cntry Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	T ⁶

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Chen et al. "High Transconductance Insulating-Gate InP/InGaAs Buried p-Buffer DH-MODFETs Grown by MOVPE." IEEE Electron Device Letters, Vol. 10, No. 4, April 1989, pp. 162-164

EXAMINER SIGNATURE: *Hea*

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dh	C01	US-5,116,774		05/26/1992	Huang et al.			
	C02	US-6,078,067		06/20/2000	Oikawa			
	C03	US-6,144,048		11/07/2000	Suemitsu et al.			
dh	C04	US-6,452,221 B1		09/17/2002	Lai et al.			
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dh	C05	JP	06120258		04-28-94	Nippon		
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
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